After Final Amendment
Not entered,
JKH
11/18.04

Amendments to the Claims

A complete list of pending claims follows, with indicated amendments:

1. (Amended)   A method for handling system management interrupts in a multiprocessor computer system, wherein each processor of the multiple processors of the computer system is ~~are~~ operable to process a system management interrupt, comprising the steps of:

writing a predetermined signature to a predetermined register of a first processor;

executing in the first processor a command of a software application to cause the first processor to initiate a system management interrupt;

receiving at each processor an instruction that the system management interrupt has been issued;

entering system management mode at each processor;

saving register contents of each processor to a memory space associated with each respective processor;

selecting from among the multiple processors ~~operable to process a system management interrupt~~ a second processor as a system management interrupt handler, the selection of the second processor ~~from among the multiple processors operable to process a system management interrupt as the system management interrupt handler~~ being accomplished according to an arbitration scheme;

scanning the contents of the memory space associated with each processor; and

when the selected second processor locates the saved predetermined signature in one of the memory spaces associated with the processors of the computers system, using the contents of the memory space associated with the predetermined signature for any parameters necessary for the handling of the system management interrupt.

2-3.   (Cancelled).